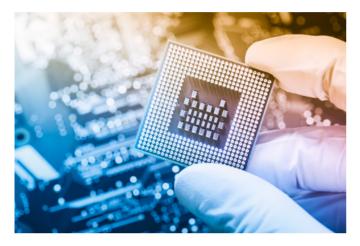
SUTD GRADUATE PROGRAMMES



SUTD-CGU Dual Masters Programme in Nano-Electronic Engineering and Design (Full-Time)



Programme Overview

The SUTD-CGU Dual Masters Programme in Nano-Electronic Engineering and Design is a unique, multidisciplinary learning experience encompassing the full value chain of the **semiconductor industry**.

This programme covers research and coursework in integrated circuit (IC) design, production, packaging, testing, reliability and failure analysis, chip assembly and packaging, particularly in the emerging areas of nano-electronic **design**, **production** and **testing**.

Industry Coursework at CGU

At CGU, students will gain real-life immersion with Taiwan's leading IC design and semiconductor foundries while undergoing CGU's **industry-oriented curriculum**.

Master Research at SUTD

At SUTD, students will complete their **Master thesis** and research project (design implementation, fabrication, testing and assembly). They will also be equipped with **industry- relevant knowledge** via SUTD's Professional Short Development Courses.

About Singapore University of Technology and Design (SUTD)

SUTD is Singapore's fourth autonomous university and one of the first universities in the world to incorporate the art and science of design and technology into a **multi-disciplinary curriculum**. SUTD's core academic pillars are:

- Architecture and Sustainable Design (ASD)
- Engineering Product Development (EPD)
- Engineering Systems and Design (ESD)
- Information Systems Technology and Design (ISTD)

About Chang Gung University (CGU)

Chang Gung University (CGU) is located in Taoyuan, Taiwan. It was ranked as the **top private university** in Taiwan by the Academic Ranking of World Universities 2016 and is well-established in semiconductor, integrated circuit design, assembly and failure analysis research.

CGU is part of the Formosa Plastics Group conglomerate and maintains strong industry links with the Taiwan electronics sector.

Upon successful completion, students will graduate with two Master degrees:

- Master of Science Nano-Electronic Engineering and Design from CGU
- Master of Engineering (Research) from SUTD

Admission Schedule / Deadlines

There is one intake each year in **September**. The application deadline for each intake is **end of March**.

Please apply at https://admission.sutd.edu.sg

Candidature Period (minimum of 18 months)

	Period 1 (at CGU)	Period 2 (at SUTD)
Candidature Period	9 months (September to June)	9 months (June to March)
Maximum candidacy	of the programme is 36 months .	

Curriculum Overview

The SUTD-CGU Dual Masters Programme starts in September at CGU and ends in March at SUTD.

Example:



To fulfill coursework requirements at CGU, students must specialise under either of the following academic tracks:

• Integrated Circuit (IC) Design Track

Under this Track, students will learn more about the tools, software and processes required to conduct analysis, design and development of a wide variety of ICs (analog, digital, radio frequency, mixed-signal).

Advanced Manufacturing Track

Under this Track, students will learn more about the integral aspects of IC production quality and reliability management. This includes reliability and failure analysis, failure mechanisms and packaging reliability.

Period 1 (9 months) at CGU	Period 2 (9 months) at SUTD
Compulsory Modules	Master Thesis and Oral Defense
 Reliability Engineering Introduction to VLSI: Technology and Design Test and Measurement for VLSI Advanced Transistor Modeling for VLSI Design 	Students will complete their research thesis and project (design implementation, fabrication, testing, assembly via close co-operation with IC foundries) while at SUTD.
Elective Modules (students must complete at least 4): IC Design Track	This Master thesis will be subject to an Oral Defense, where students will defend their research against professional critique by a board consisting of SUTD faculty and external faculty/industry experts.
Digital Electronics Analog Electronics RF IC Design	Professional Development Short Courses Students are required to complete at least 4 Professional
 Mixed Signal Design Bio-Medical Electronics* Nano-Materials and Devices* 	 Short Development Courses. Topics covered include: Intellectual Property Clinic Scientific and Grant Writing
Advanced Manufacturing Track • Failure Analysis for VLSI • Failure Mechanisms in VLSI • IC Packaging Reliability • Quality Engineering	 Entrepreneurship Private Equity & Fund Raising for Businesses Corporate Grooming & Personal Branding Leadership & Teamwork
 Bio-Medical Electronics* Nano-Materials and Devices* 	View a full list of courses and course overviews at
*Students can choose either Bio-Medical Electronics or Nano-Materials and Devices, but not both.	https://sutd.edu.sg/Admissions/Graduate/ Professional-Development-Short-Courses

Admission Requirements

Applicants should possess the following:

•At least a **Bachelor's degree** or above with excellent academic standing in mathematics, physics, engineering, computer science or related fields.

•Proficiency in English (**IELTS** or **TOEFL** is required if English is not your medium of instruction in your studies)

Contact Us

For admission and programme enquiries:

SUTD Office of Graduate Studies: MNEED@sutd.edu.sg

View SUTD Faculty's Research Here:

- EPD <u>https://epd.sutd.edu.sg/people/faculty/</u>
- ESD https://esd.sutd.edu.sg/people/faculty/
- ISTD https://istd.sutd.edu.sg/people/faculty/
- Sci & https://academics.sutd.edu.sg/science-math/
- Math science-faculty/